

TOSHIBA FIELD EFFECT TRANSISTOR SILICON N CHANNEL MOS TYPE ( $\pi$ -MOSV)

# 2SK2952

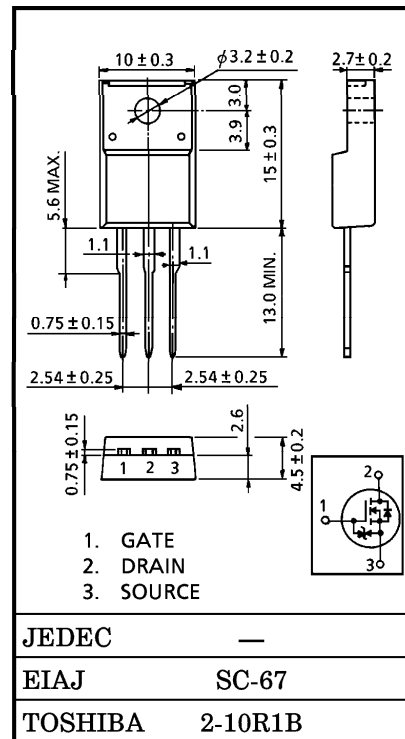
HIGH SPEED, HIGH CURRENT SWITCHING APPLICATIONS  
 CHOPPER REGULATOR APPLICATIONS

INDUSTRIAL APPLICATIONS  
 Unit in mm

- Low Drain-Source ON Resistance :  $R_{DS(ON)} = 0.4\Omega$  (Typ.)
- High Forward Transfer Admittance :  $|Y_{fs}| = 8.0S$  (Typ.)
- Low Leakage Current :  $I_{DSS} = 100\mu A$  (Max.)  
( $V_{DS} = 400V$ )
- Enhancement-Mode :  $V_{th} = 2.0 \sim 4.0V$   
( $V_{DS} = 10V, I_D = 1mA$ )

MAXIMUM RATINGS ( $T_a = 25^\circ C$ )

CHARACTERISTIC		SYMBOL	RATING	UNIT
Drain-Source Voltage		$V_{DSS}$	400	V
Drain-Gate Voltage ( $R_{GS} = 20k\Omega$ )		$V_{DGR}$	400	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	DC	$I_D$	8.5	A
	Pulse	$I_{DP}$	34	A
Drain Power Dissipation ( $T_c = 25^\circ C$ )		$P_D$	40	W
Single Pulse Avalanche Energy**		$E_{AS}$	427	mJ
Avalanche Current		$I_{AR}$	8.5	A
Repetitive Avalanche Energy*		$E_{AR}$	4.0	mJ
Channel Temperature		$T_{ch}$	150	$^\circ C$
Storage Temperature Range		$T_{stg}$	$-55 \sim 150$	$^\circ C$



Weight : 1.9g (Typ.)

THERMAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	$R_{th(ch-c)}$	3.125	$^\circ C/W$
Thermal Resistance, Channel to Ambient	$R_{th(ch-a)}$	62.5	$^\circ C/W$

Note ;

\* Repetitive rating ; Pulse Width Limited by Max. junction temperature.

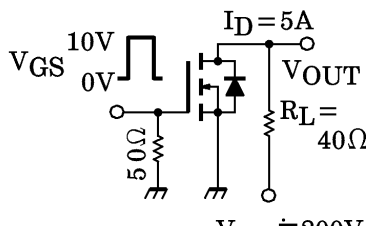
\*\*  $V_{DD} = 90V$ , Starting  $T_{ch} = 25^\circ C$ ,  $L = 9.6mH$ ,  $R_G = 25\Omega$ ,  $I_{AR} = 8.5A$

**This transistor is an electrostatic sensitive device.  
 Please handle with caution.**

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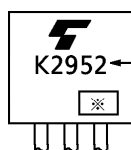
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±25V, V <sub>DS</sub> = 0V	—	—	±10	μA	
Gate-Source Breakdown Voltage	V (BR) GSS	I <sub>G</sub> = ±10μA, V <sub>DS</sub> = 0V	±30	—	—	V	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 400V, V <sub>GS</sub> = 0V	—	—	100	μA	
Drain-Source Breakdown Voltage	V (BR) DSS	I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0V	400	—	—	V	
Gate Threshold Voltage	V <sub>th</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 1mA	2.0	—	4.0	V	
Drain-Source ON Resistance	R <sub>D(S) (ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A	—	0.4	0.55	Ω	
Forward Transfer Admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 5A	4.0	8.0	—	S	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1MHz	—	1340	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		—	160	—		
Output Capacitance	C <sub>oss</sub>		—	490	—		
Switching Time	Rise Time	t <sub>r</sub>		—	22	—	ns
	Turn-On Time	t <sub>on</sub>		—	60	—	
	Fall Time	t <sub>f</sub>		—	32	—	
	Turn-Off Time	t <sub>off</sub>		—	140	—	
Total Gate Charge (Gate-Source Plus Gate-Drain)	Q <sub>g</sub>	V <sub>DD</sub> ≐ 320V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 8.5A	—	34	—	nC	
Gate-Source Charge	Q <sub>gs</sub>		—	18	—		
Gate-Drain ("Miller") Charge	Q <sub>gd</sub>		—	16	—		

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	I <sub>DR</sub>	—	—	—	8.5	A
Pulse Drain Reverse Current	I <sub>DRP</sub>	—	—	—	34	A
Diode Forward Voltage	V <sub>DSF</sub>	I <sub>DR</sub> = 8.5A, V <sub>GS</sub> = 0V	—	—	-1.7	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>DR</sub> = 8.5A, V <sub>GS</sub> = 0V dI <sub>DR</sub> / dt = 100A / μs	—	350	—	ns
Reverse Recovery Charge	Q <sub>rr</sub>		—	2.6	—	μC

MARKING

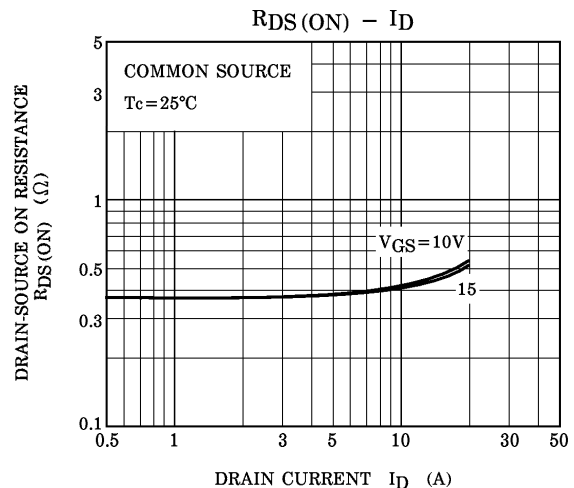
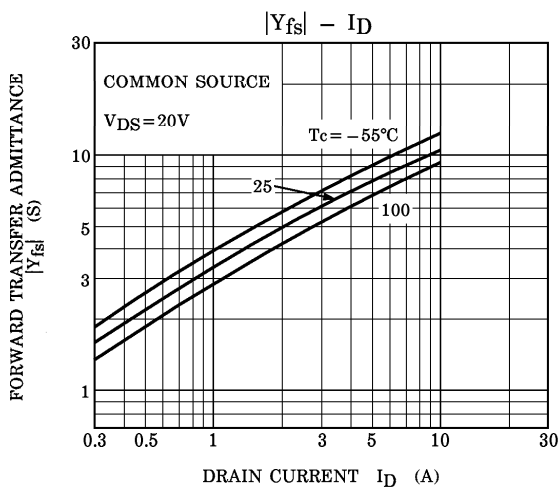
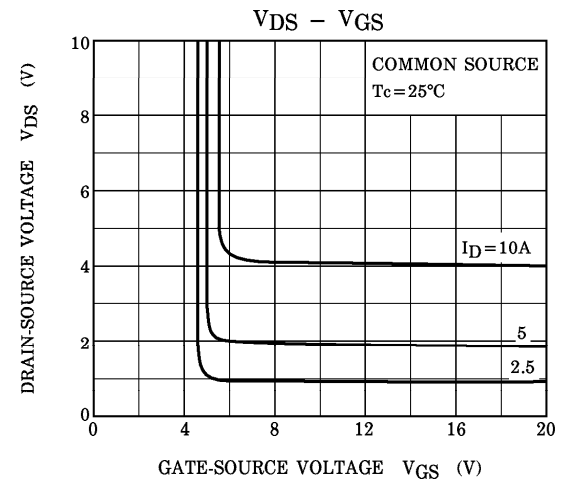
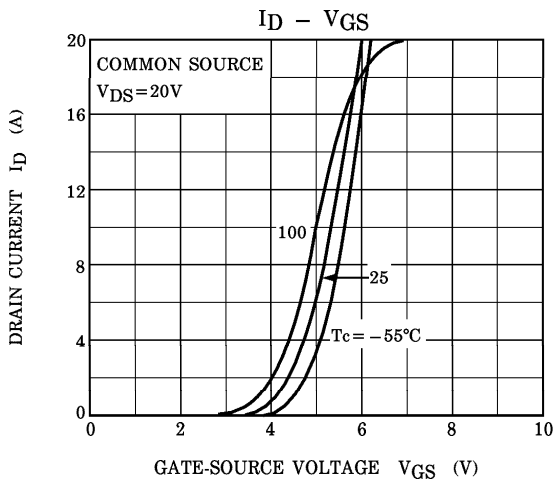
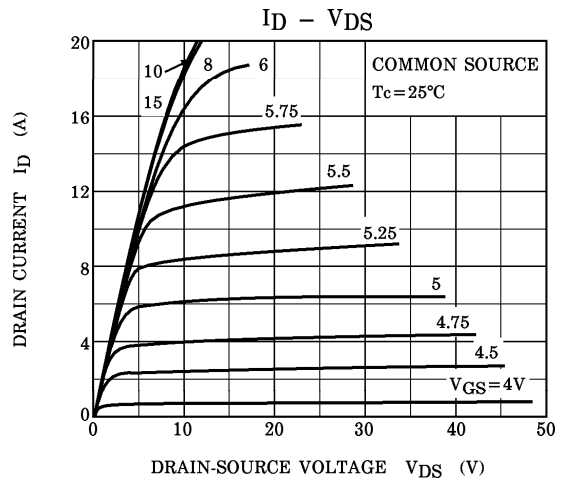
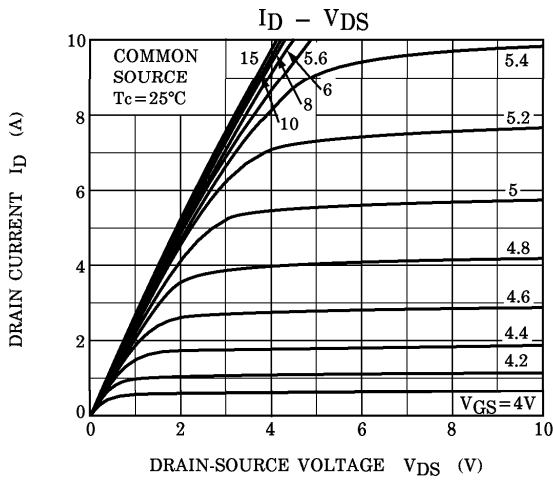


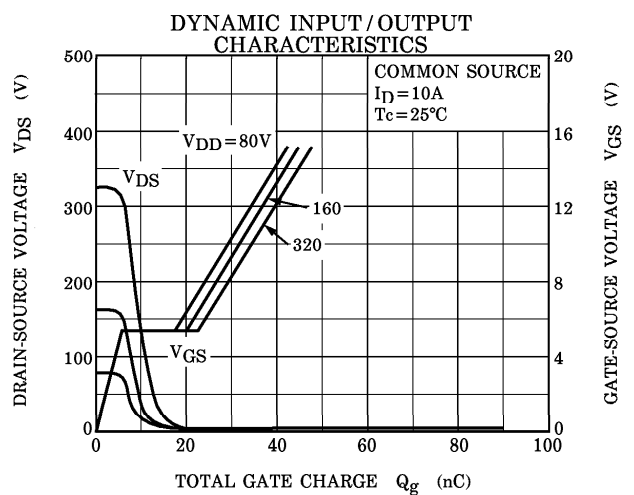
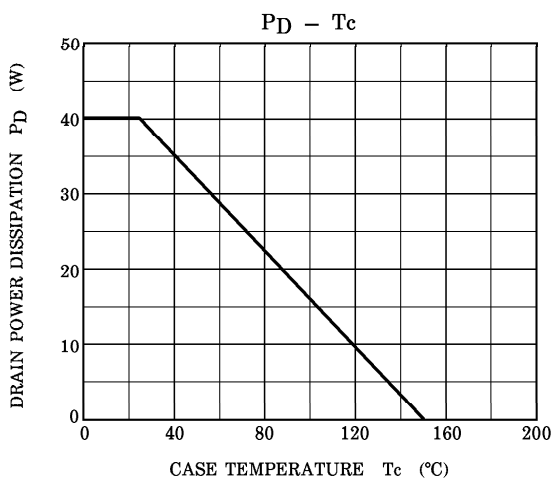
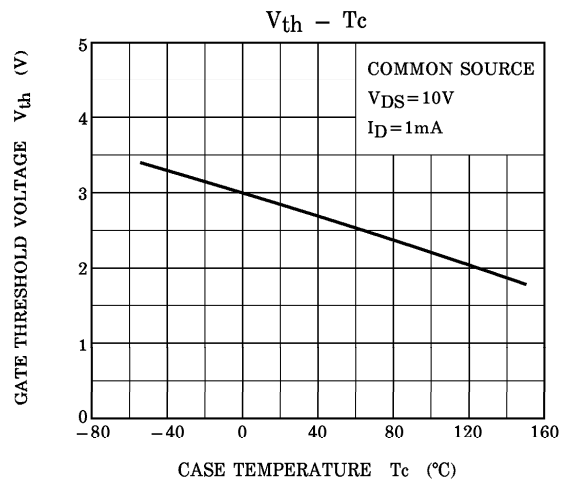
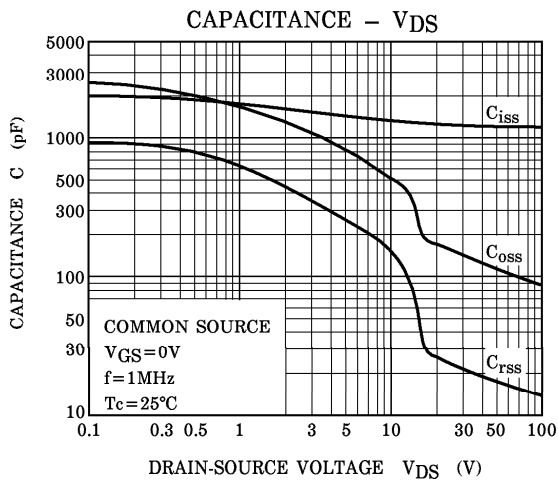
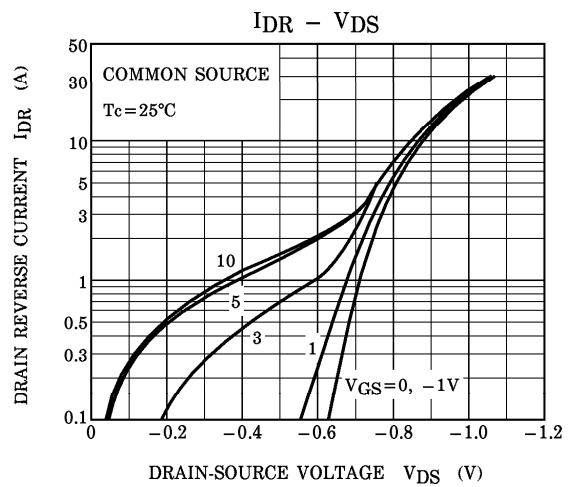
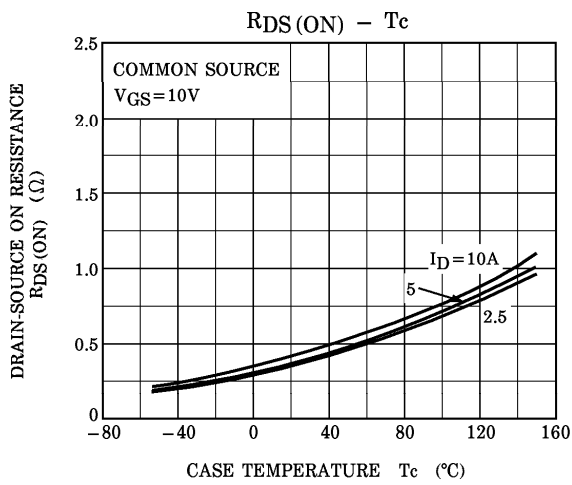
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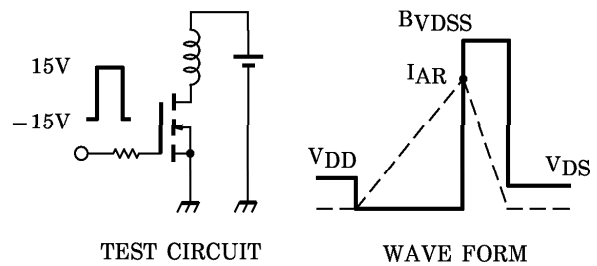
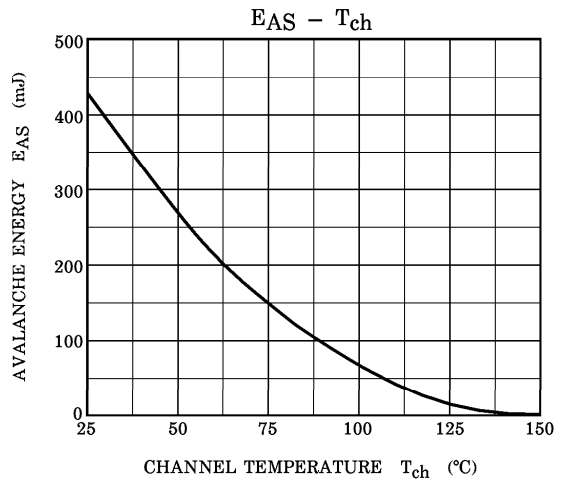
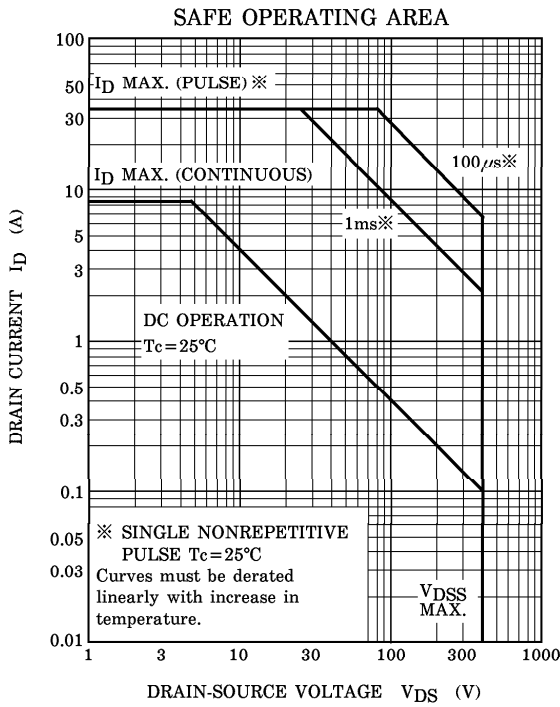
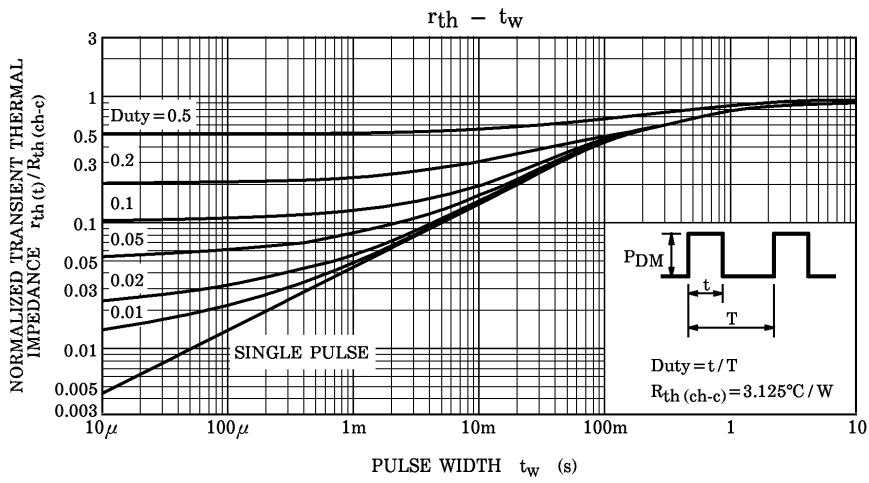
※ Lot Number

□ □ — Month (Starting from Alphabet A)

— Year (Last Number of the Christian Era)







Peak  $I_{AR} = 8.5A$ ,  $R_G = 25\Omega$   
 $V_{DD} = 90V$ ,  $L = 9.6mH$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$